

V. REMARKS

Claim 5 is rejected under 35 USC 112, second paragraph, as indefinite for allegedly failing to particularly point out and distinctly claimed the subject matter of the invention. The claim is amended to obviate the rejection. Withdrawal of the rejection is respectfully requested.

Claims 1-5 are rejected under 35 USC 103 (a) as being unpatentable over Masumori et al. (U.S. Patent No. 5,168,270). The rejection is respectfully traversed.

Masumori teaches a liquid crystal display device capable of selecting display definition modes, and driving method therefore. An input analog image signal is sampled by first and second A/D converters using first and second sampling clocks of the same period to obtain digital gradation data. In the case of a double definition display mode, the first and second sampling clocks are made 180° out of phase with each other and the output of the first A/D converter is delayed for one-half period, by which its timing is brought into agreement with that of the output of the second A/D converter, thus obtaining a pair of digital gradation data. In the case of a standard definition display mode, the first and second sampling clocks of the same phase are used to obtain the outputs of the first and second A/D converters as a pair of digital gradation data. The pair of digital gradation data Da and Db is converted by a signal processing part into a pair of analog gradation data Aa and Ab, which is subjected to a serial-to-parallel conversion by a source driver to be supplied in parallel to data lines. In the double definition display mode the gate driver sequentially drives odd-numbered row lines in odd-numbered frames and even-numbered row lines in even-numbered frames. In the standard definition display mode every two adjacent row lines are simultaneously driven in a sequential order.

Claim 1, as amended, is directed to a display device provided with a

gaming machine that includes a plurality of pixels, a drive controller and information signal lines. Claim 1 recites that the plurality of pixels are arranged in a matrix on an xy plane such that the pixels extend in an x direction and a y direction. Claim 1 further recites that each pixel includes a first pixel unit and a second pixel unit disposed in a juxtaposed relationship with the first pixel unit. Also, claim 1 recites that each one of the first and second pixel units includes at least a first pixel electrode corresponding to a first pixel electrode color and a second pixel electrode corresponding to a second pixel electrode color that is different from the first pixel electrode color. Additionally, claim 1 recites that the first and second pixel electrodes are arranged in a side-by-side manner and extend in the x direction to form a first-and-second pixel electrode arrangement with the first-and-second pixel electrode arrangement being identical for both the first and second pixel units. Further, claim 1 recites that the drive controller controls an information signal supplied to the pixel electrodes and information signal lines connect outputs for one pixel from the drive controller such that, when a selected one of the plurality of pixels is activated, the pixel electrodes corresponding to the same pixel electrode color are simultaneously activated.

It is respectfully submitted that none on the applied art, alone or in combination, teaches or suggests the features of claim 1 as amended and discussed immediately above. Thus, it is respectfully submitted that one of ordinary skill in the art would not be motivated to modify the features of the applied art because such reference is devoid of such features. As a result, it is respectfully submitted that claim 1 is allowable over the applied art.

Claim 2, as amended, is similar to claim 1, as amended. Further, claim 2 recites that information signal lines that connect outputs for one pixel from the drive controller with pixel electrodes for the same color included in a pair of the pixel units respectively in a one-to-two relationship such that, when a selected one of the plurality of pixels is activated, the pixel electrodes corresponding to the

same pixel electrode color are simultaneously activated.

It is respectfully submitted that the applied art fails to teach or suggest the features of claim 2 as amended and discussed. Specifically, it is respectfully submitted that the applied art fails to teach pixel electrodes for the same color are included in a pair of the pixel units respectively in a one-to-two relationship. Thus, one of ordinary skill in the art would not be motivated to modify the features of the applied art because such features are devoid in the applied art. As a result, it is respectfully submitted that claim 2 is allowable over the applied art.

Claim 3, as we propose to amend it, is directed to a display device provided with a gaming machine that includes pixel units, a drive controller and information signal lines. Claim 3 recites that each pixel unit is formed by arranging each one of a plurality of kinds of pixel electrodes which display predetermined colors respectively and the drive controller which controls an information signal supplied to the pixel electrodes. Claim 3 further recites that the information signal lines connect outputs for one pixel from the drive controller with the pixel electrodes for the same color included in a pair of pixel units respectively in a one-to-many relationship. Furthermore, claim 3 recites that the information signal lines are wired in a x direction and connected to an information signal driver and gate lines are wired in a y direction and connected to a scanning signal driver. Additionally, claim 3 recites that the gate lines and the information lines are orthogonal in condition of being insulated mutually and one pixel is constituted by a pair of adjacent pixel units.

It is respectfully submitted that the applied art fails to teach or suggest the features of claim 3 as amended. Specifically, it is respectfully submitted that the applied art fails to teach that the information signal lines are wired in a x direction and connected to an information signal driver and gate lines are wired in a y

direction and connected to a scanning signal driver with the gate lines and the information lines being orthogonal in condition of being insulated mutually. Thus, one of ordinary skill in the art would not be motivated to modify the features of the applied art because such features are devoid in the applied art. As a result, it is respectfully submitted that claim 3 is allowable over the applied art.

Claim 4 depends from claim 1 and includes all of the features of claim 1. Thus, it is respectfully submitted that the dependent claim is allowable at least for the reasons claim 4 is allowable as well as for the features it recites.

Claim 5 depends from claim 2 and includes all of the features of claim 2. Thus, it is respectfully submitted that the dependent claim is allowable at least for the reasons claim 2 is allowable as well as for the features it recites.

In particular, with regard to claims 3-5, since the information signal lines wired in the x direction and connected to the information signal driver 10 wired in the y direction and connected to this scanning signal driver 11 are isolated mutually, signal delay caused by other including noise etc. may be prevented.

It is respectfully submitted that the pending claims are believed to be in condition for allowance over the prior art of record. Therefore, this Amendment is believed to be a complete response to the outstanding Office Action. Further, Applicants assert that there are also reasons other than those set forth above why the pending claims are patentable. Applicants hereby reserve the right to set forth further arguments and remarks supporting the patentability of their claims, including the separate patentability of the dependent claims not explicitly addressed herein, in future papers.

In view of the foregoing, reconsideration of the application and allowance of the pending claims are respectfully requested. Should the Examiner believe

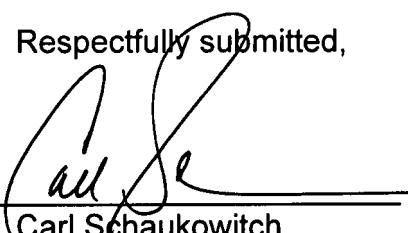
anything further is desirable in order to place the application in even better condition for allowance, the Examiner is invited to contact Applicants' representative at the telephone number listed below.

Should additional fees be necessary in connection with the filing of this paper or if a Petition for Extension of Time is required for timely acceptance of the same, the Commissioner is hereby authorized to charge Deposit Account No. 18-0013 for any such fees and Applicant(s) hereby petition for such extension of time.

Date: September 6, 2007

Respectfully submitted,

By:

  
Carl Schaukowitch  
Reg. No. 29,211

**RADER, FISHMAN & GRAUER PLLC**  
1233 20<sup>th</sup> Street, N.W. Suite 501  
Washington, D.C. 20036  
Tel: (202) 955-3750  
Fax: (202) 955-3751  
Customer No. 23353

Enclosure(s):      Amendment Transmittal  
                         Petition for Extension of Time (three months)

DC287765.DOC